

Figure 1

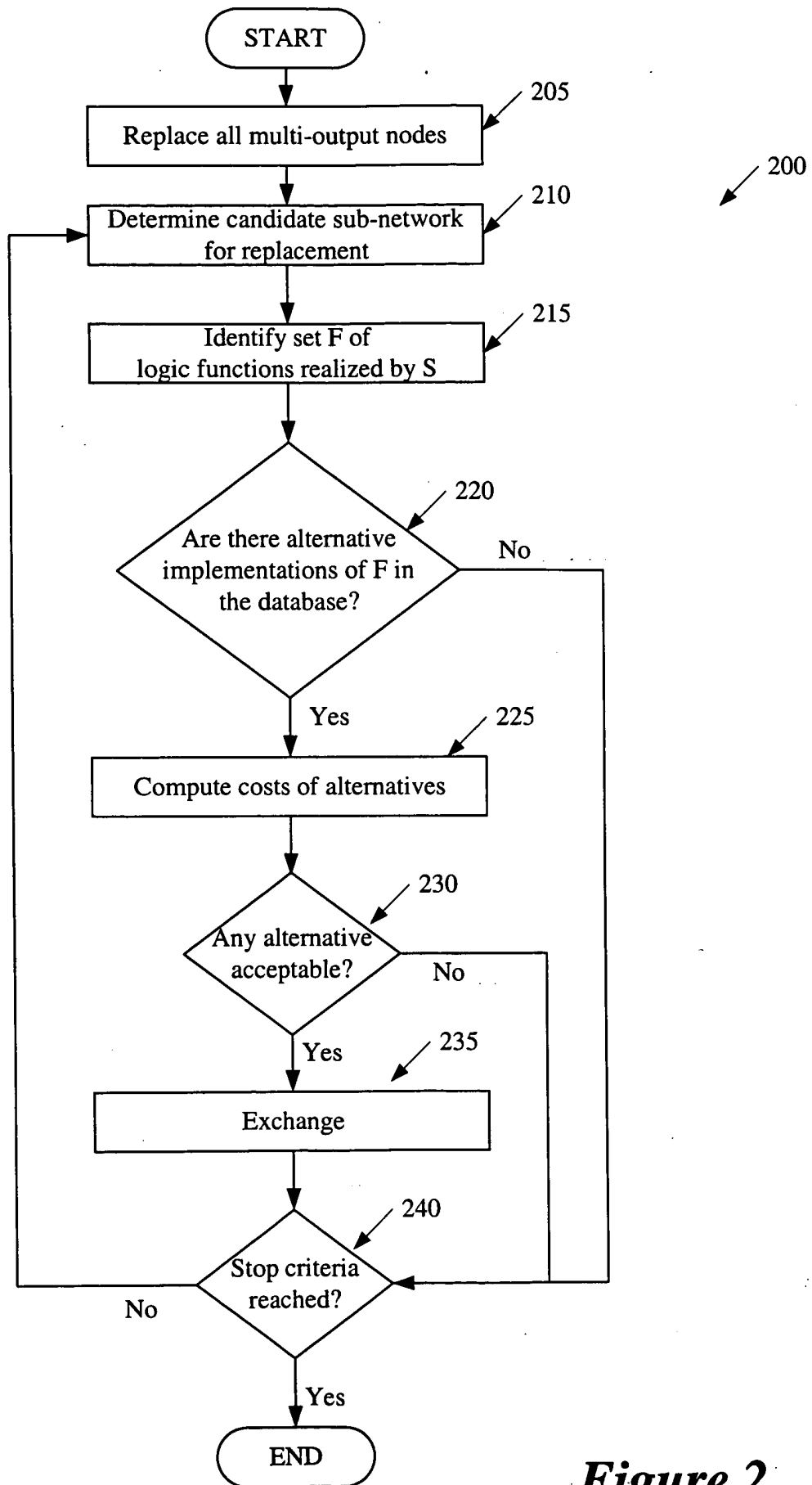


Figure 2

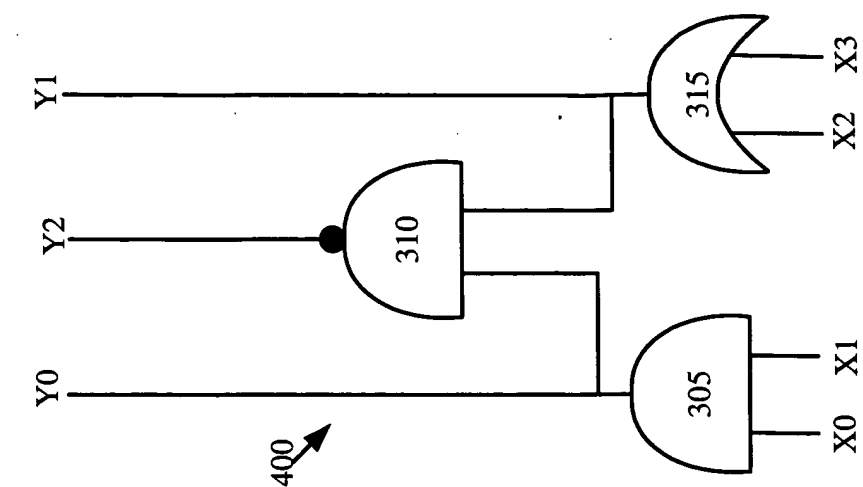
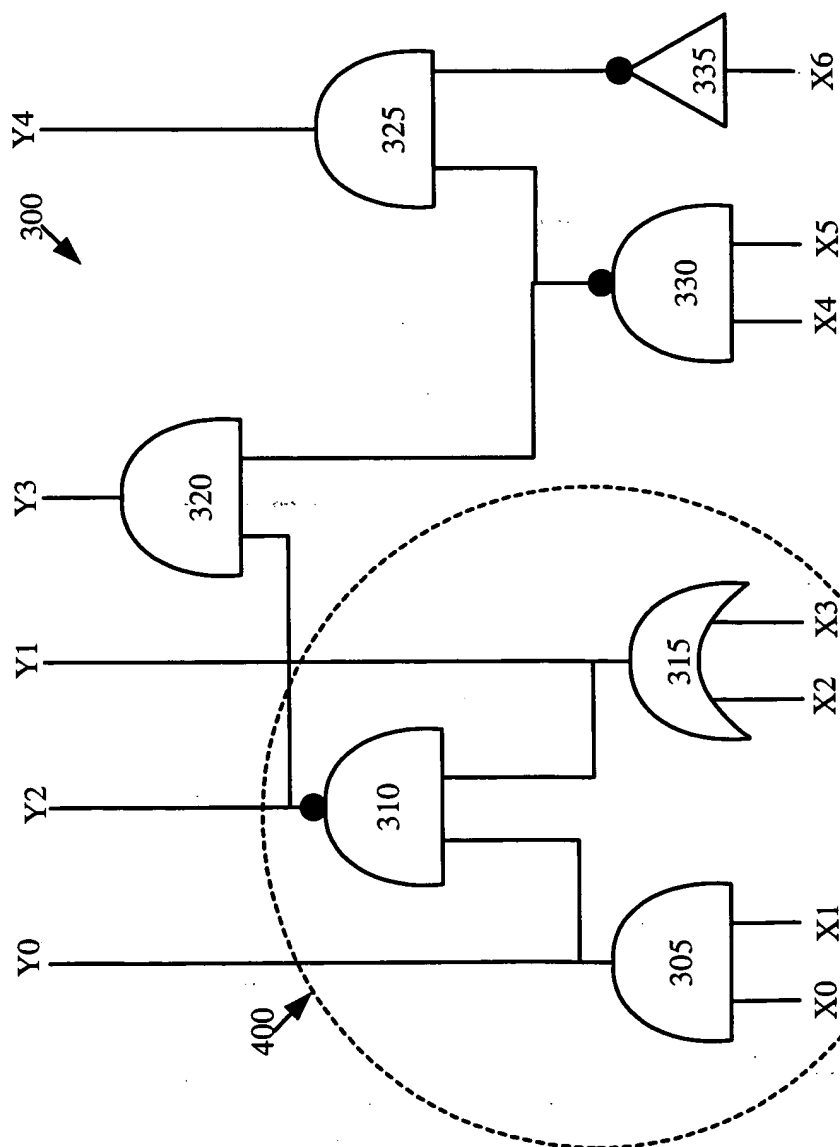
[illegible]

Figure 4



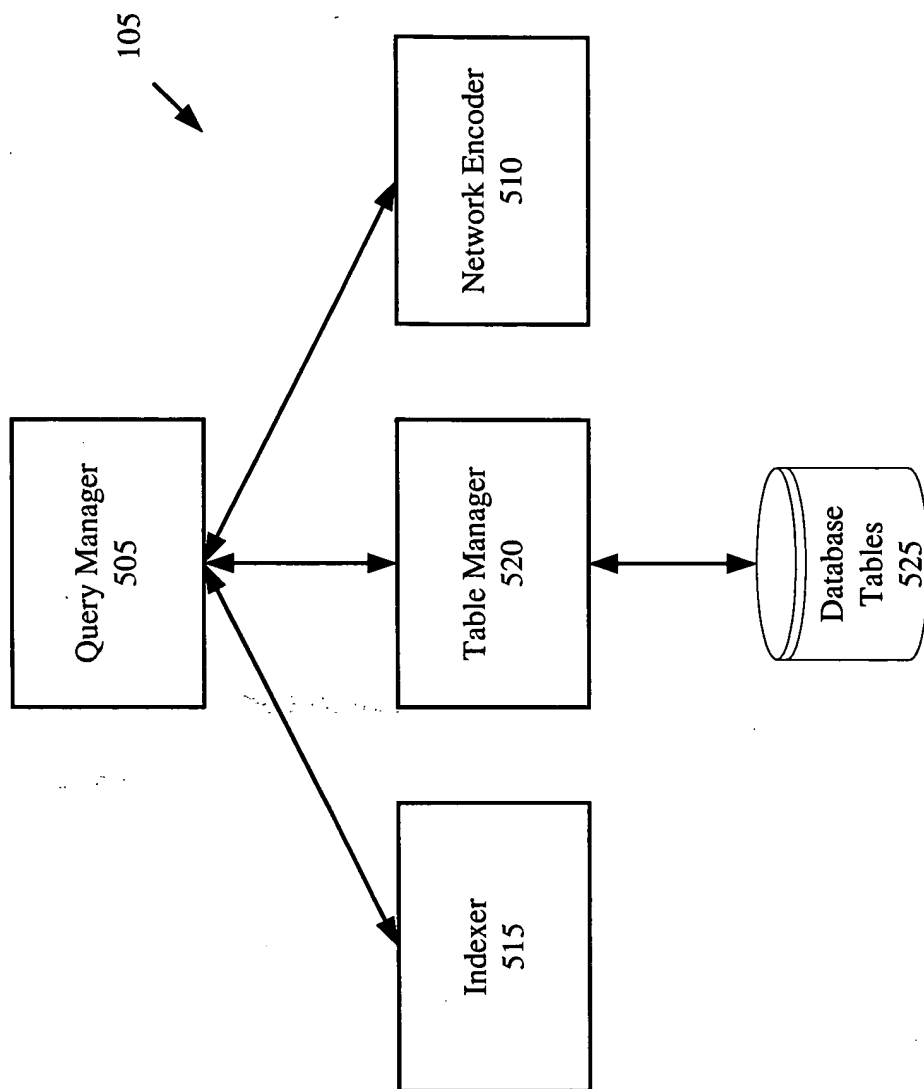


Figure 5

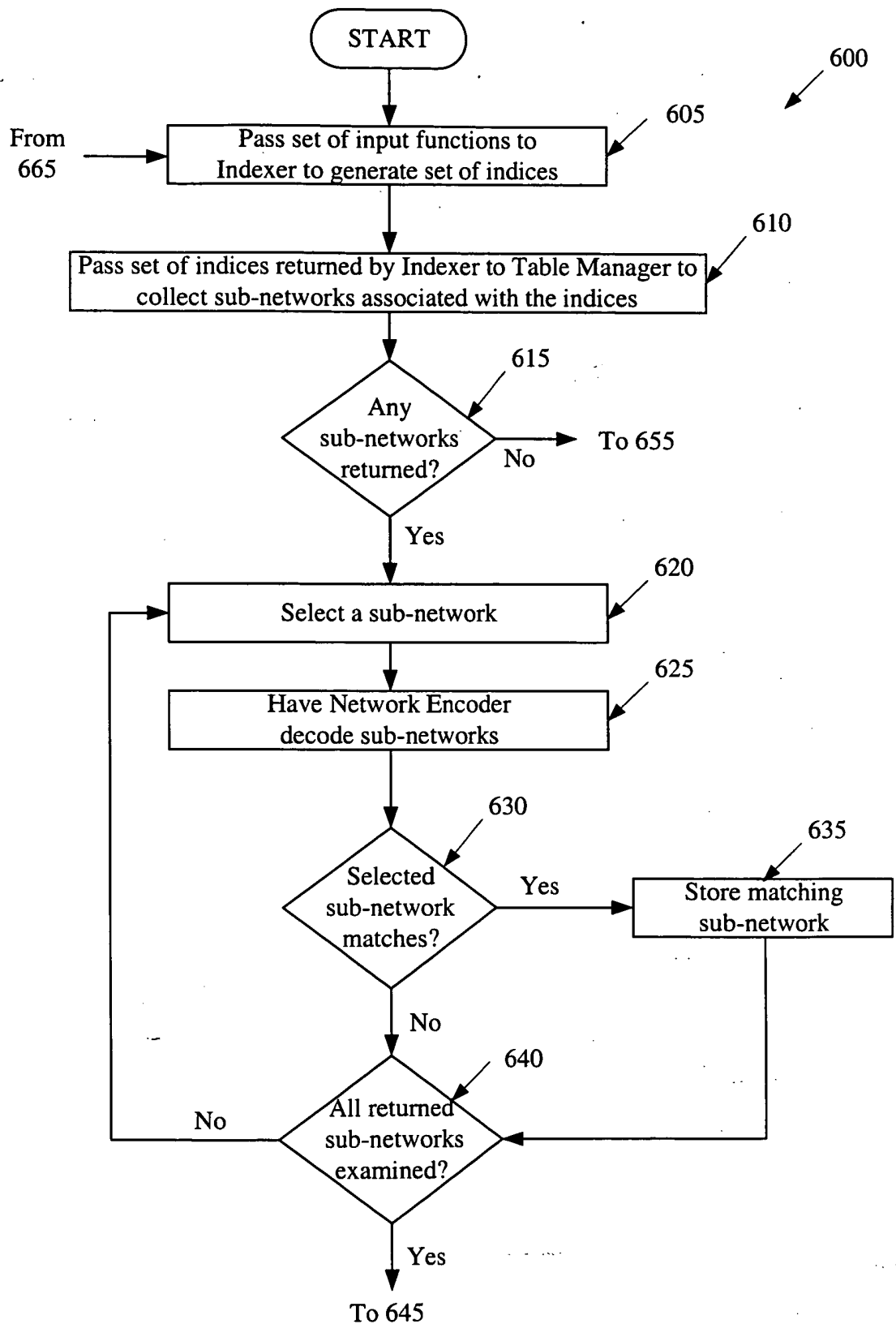


Figure 6A

Figure 6: Figure 6A
Figure 6B

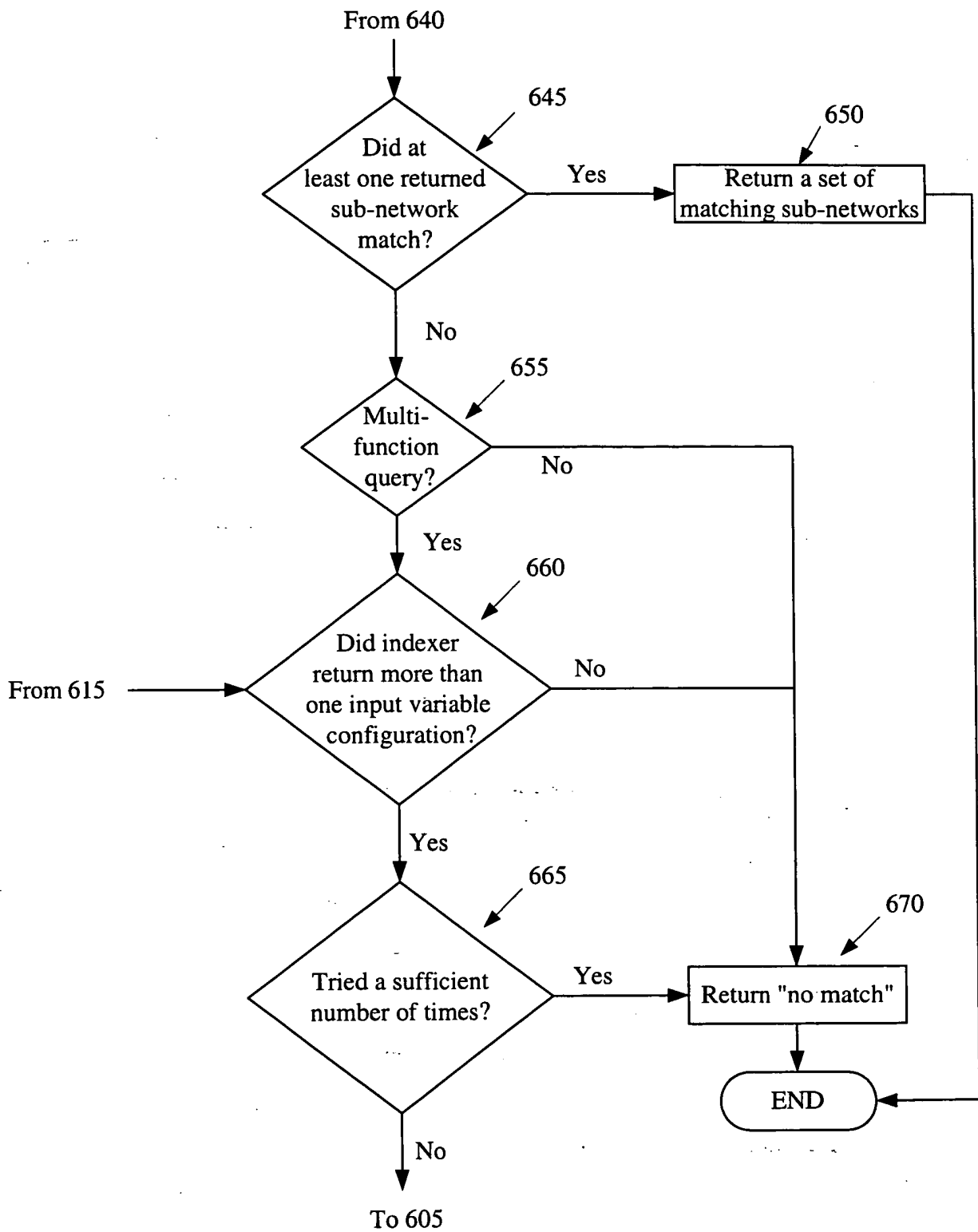


Figure 6B

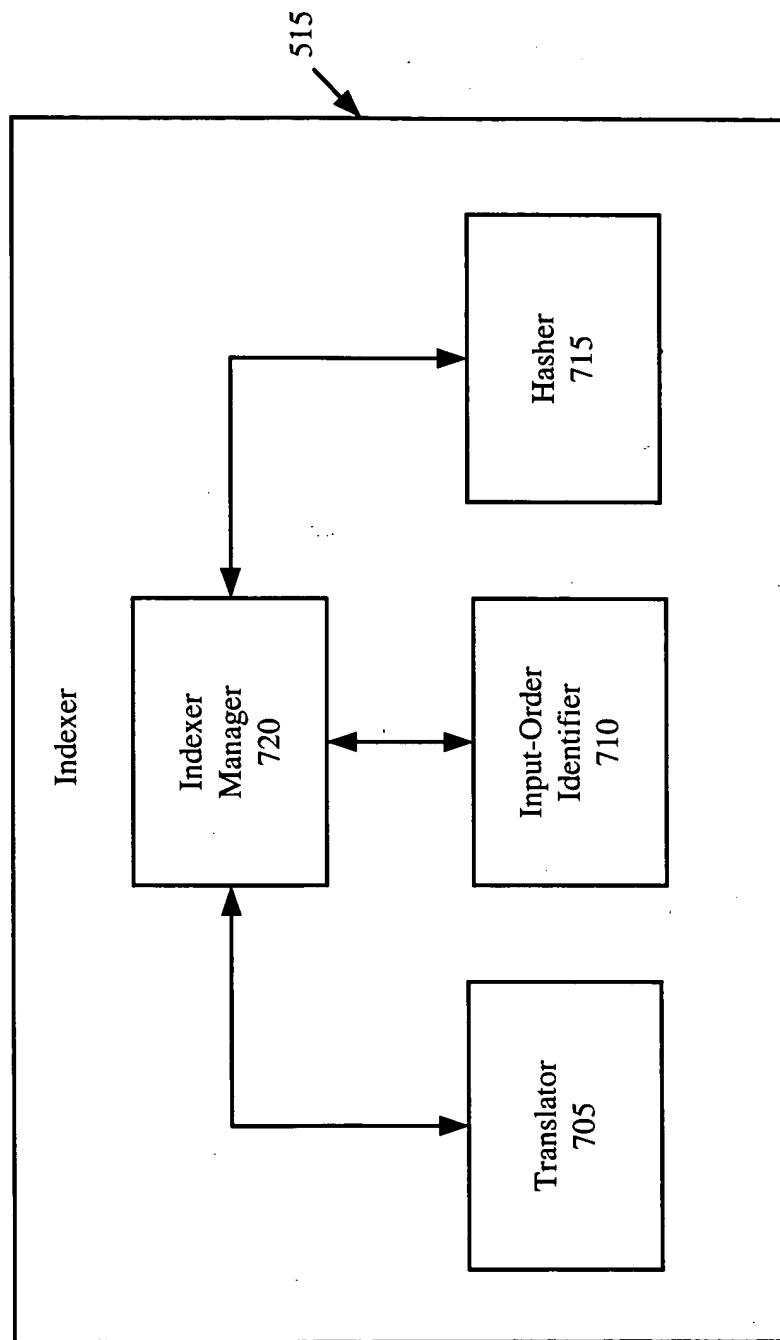


Figure 7

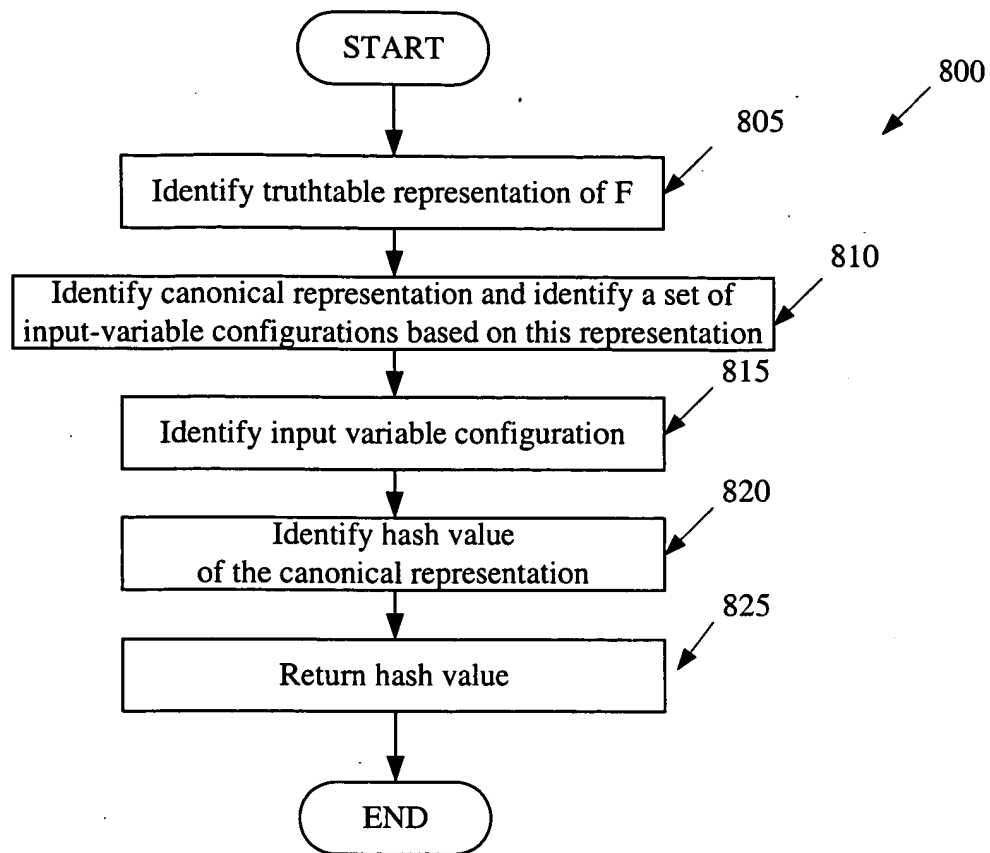


Figure 8

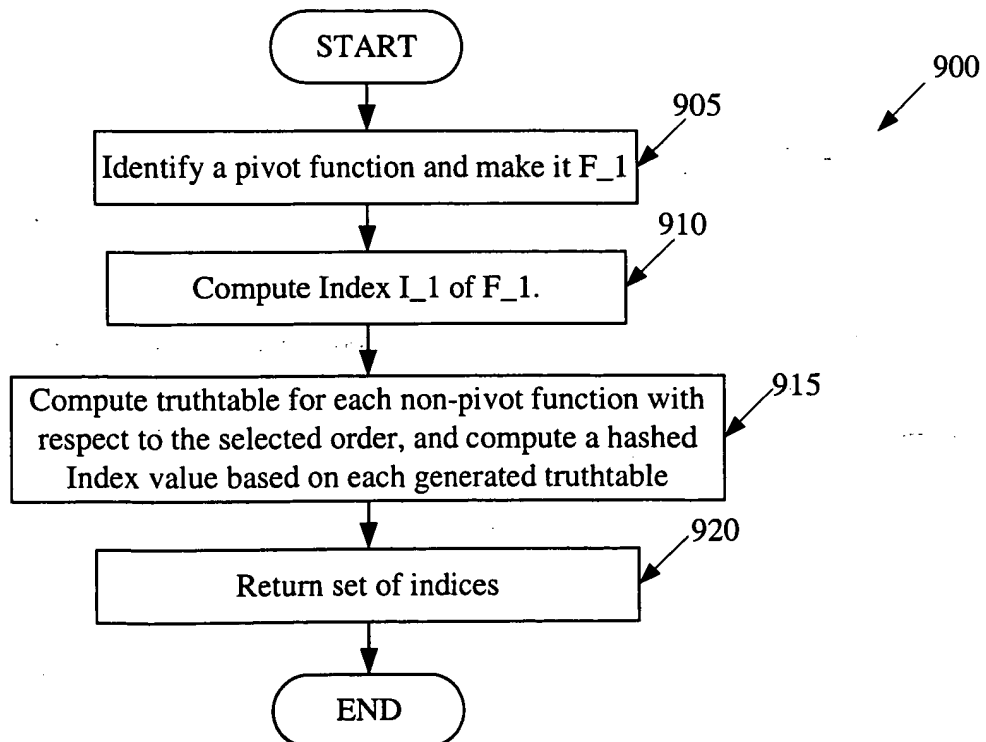


Figure 9

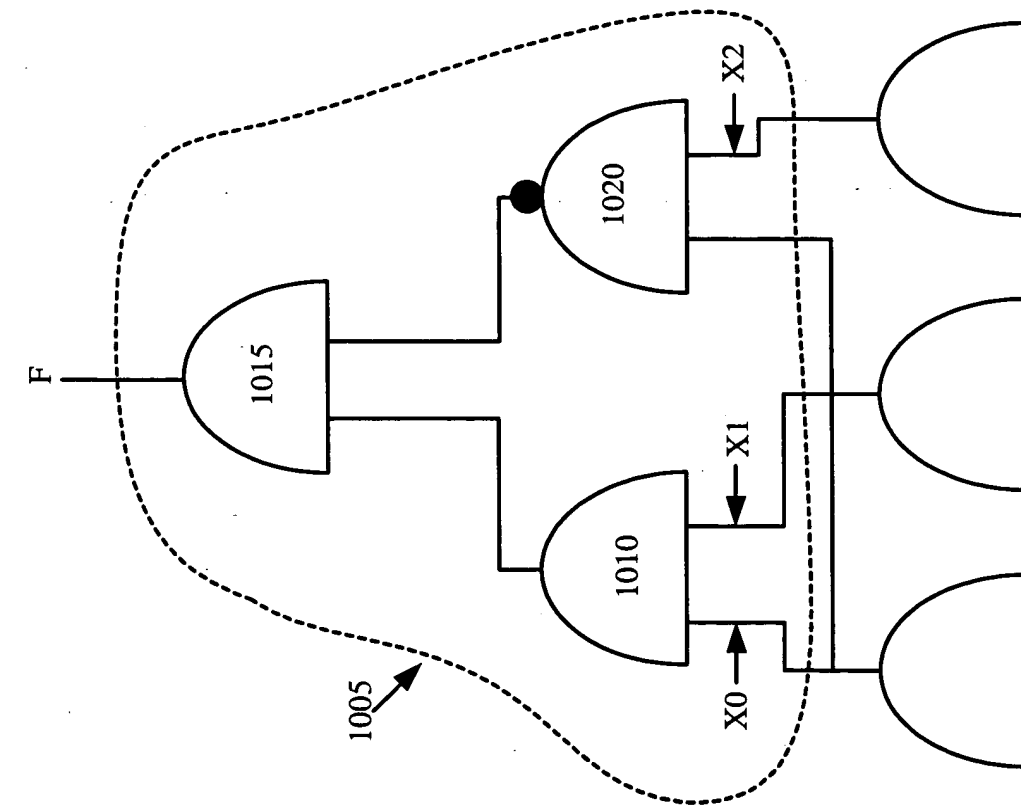


Figure 10

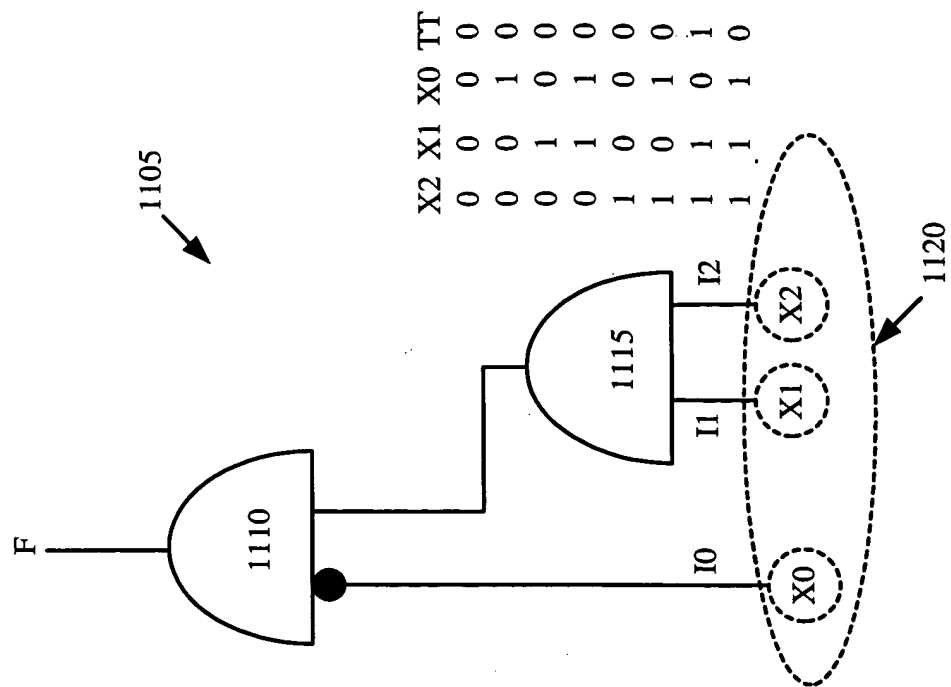
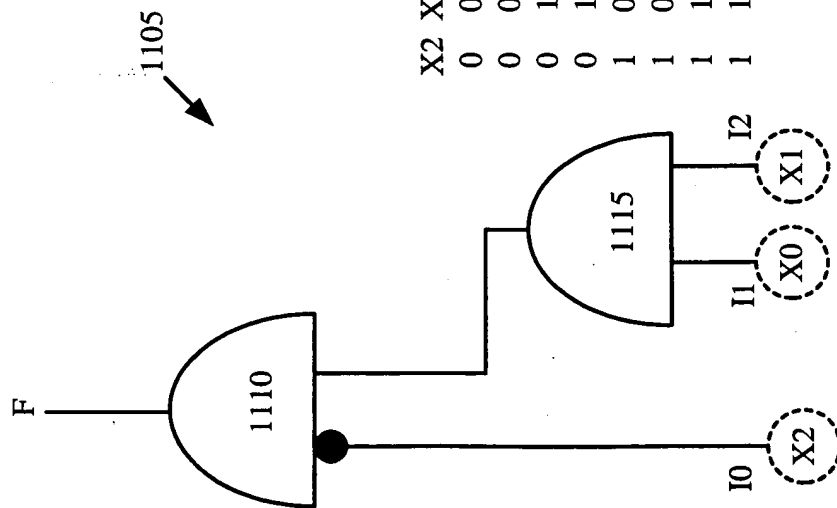
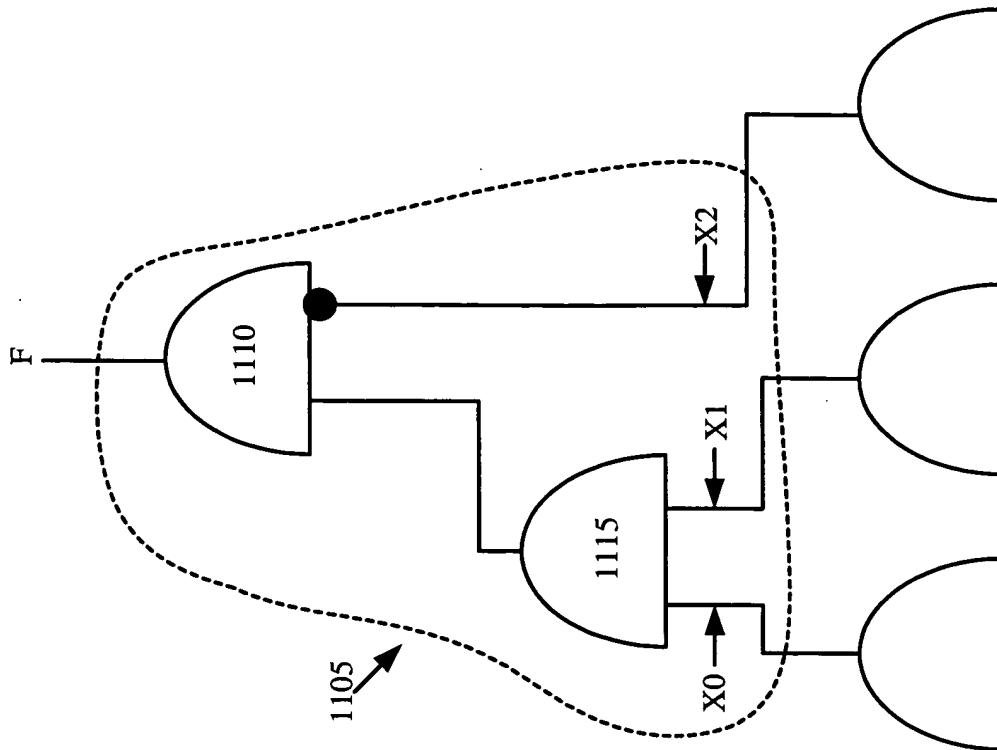


Figure 11



X2	X1	X0	TT
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

Figure 12



X2	X1	X0	TT
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

Figure 13

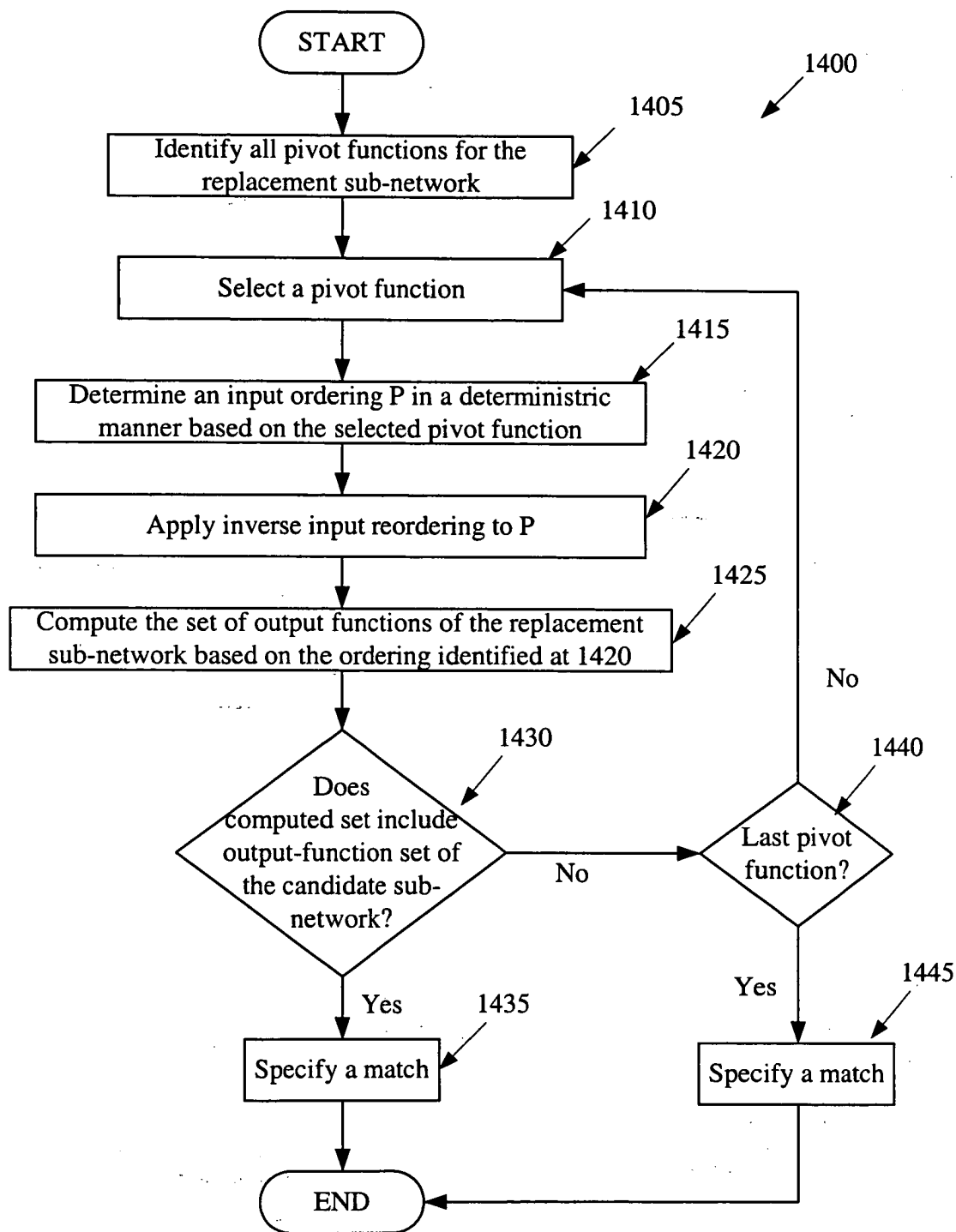


Figure 14

FIG. 15

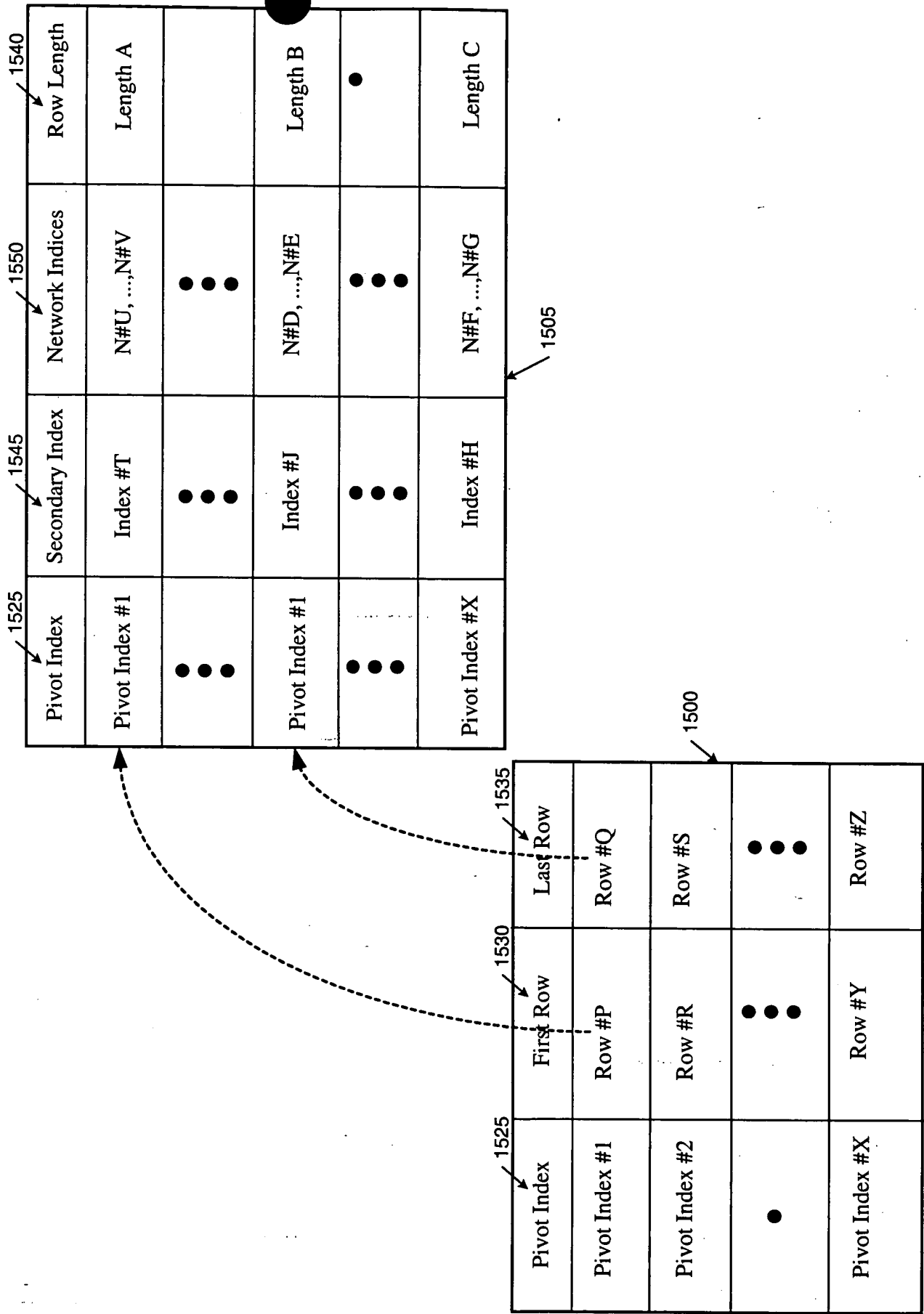


Figure 15

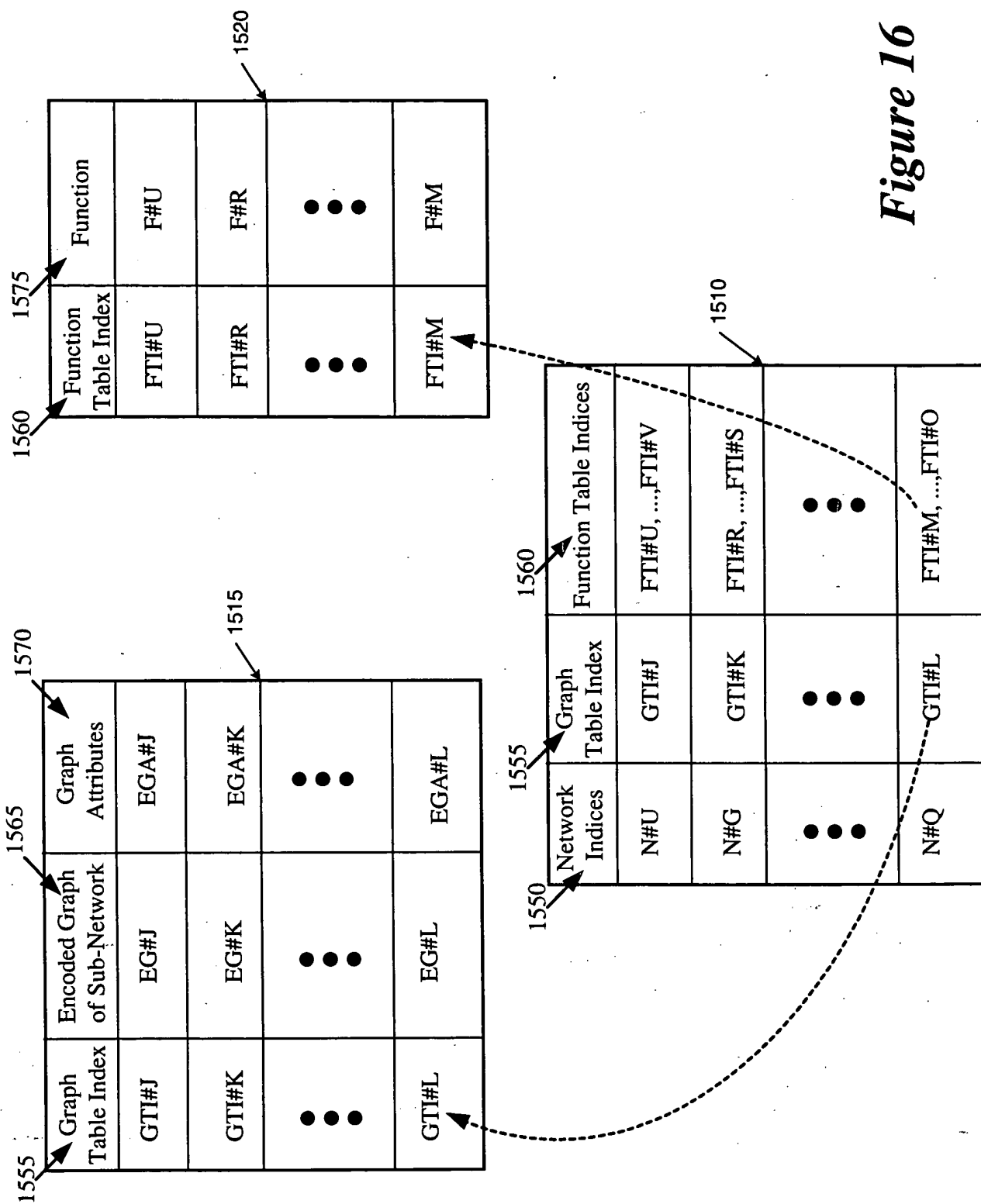


Figure 16

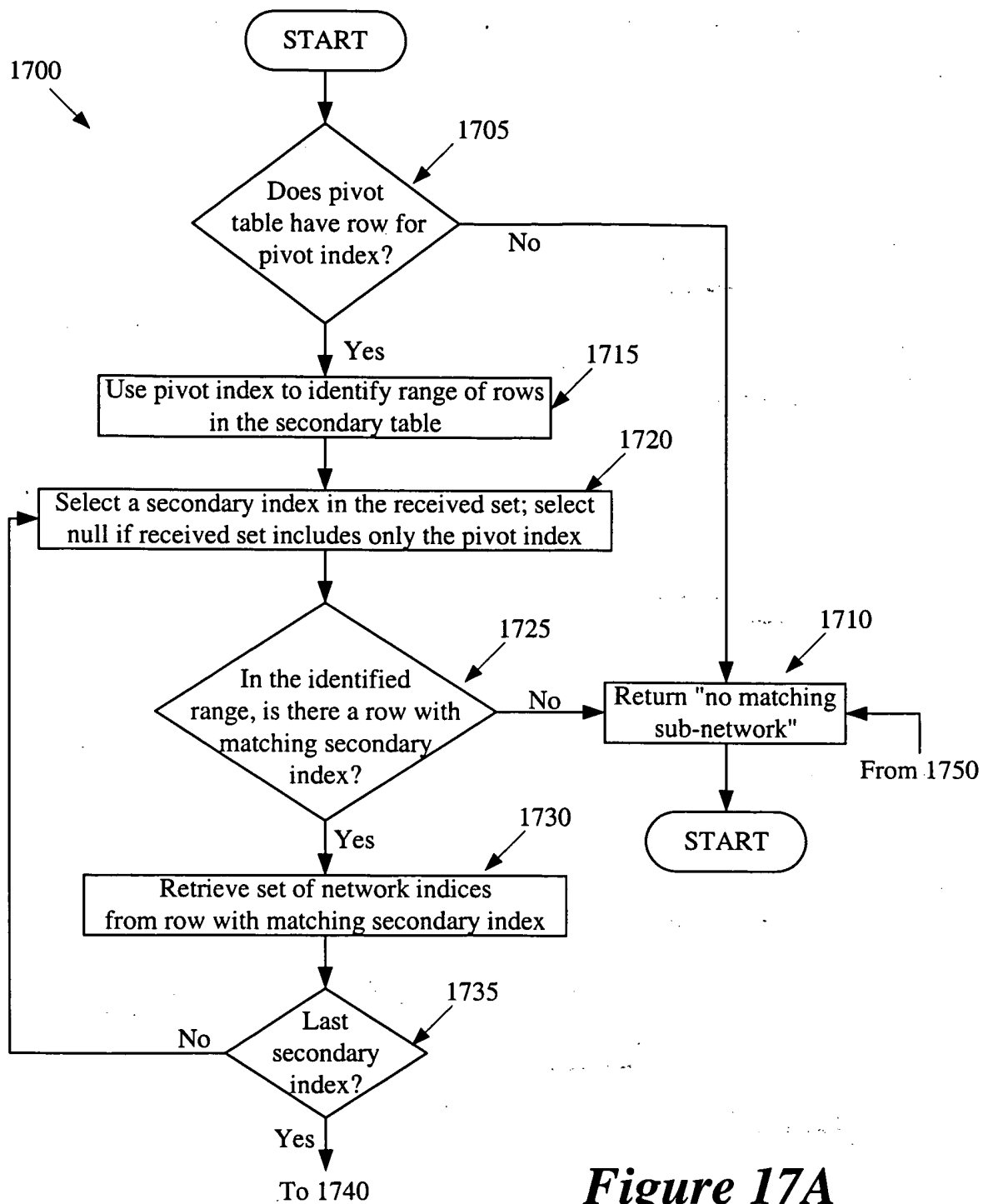


Figure 17A

Figure 17: *Figure 17A*
Figure 17B

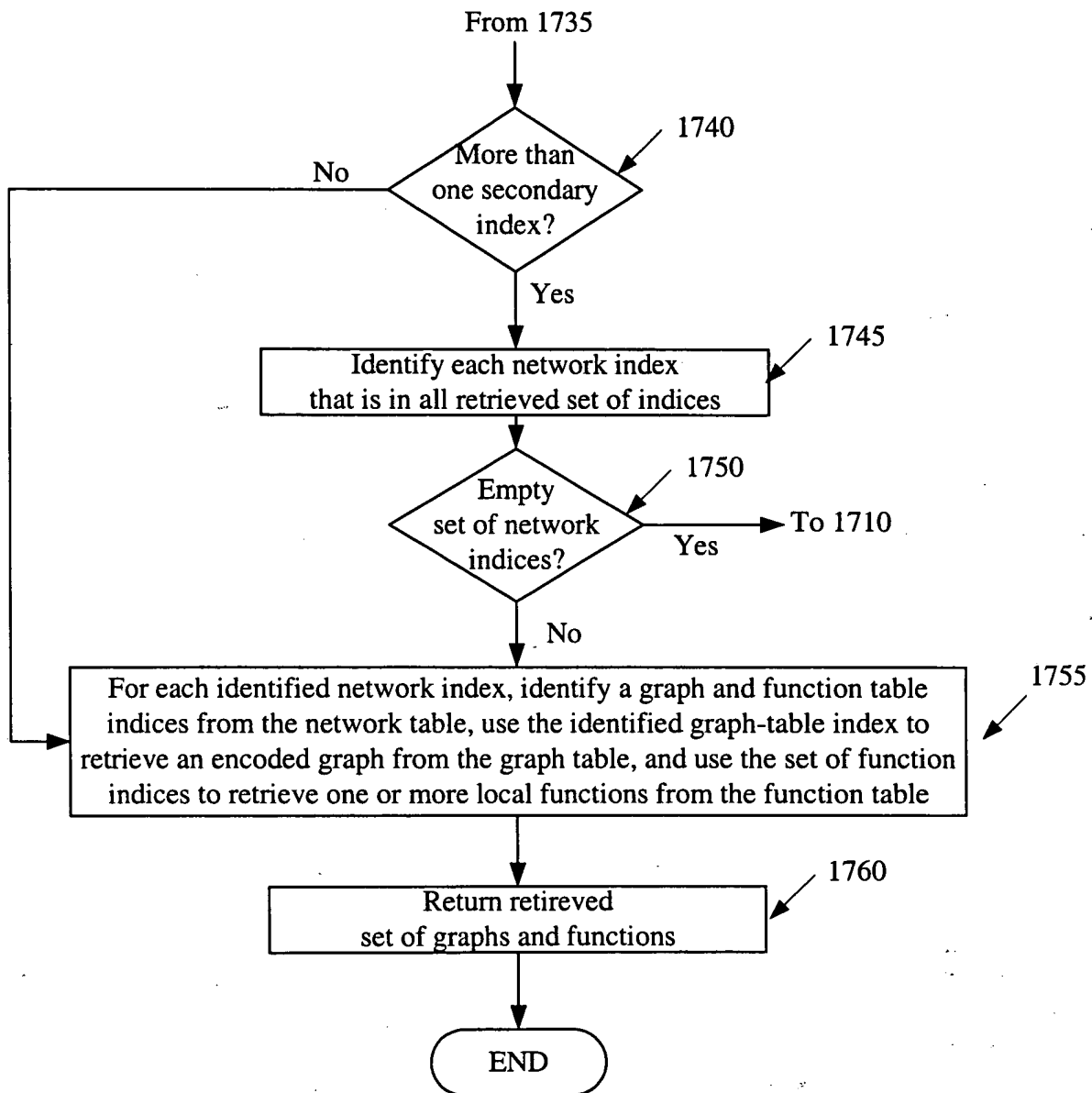


Figure 17B

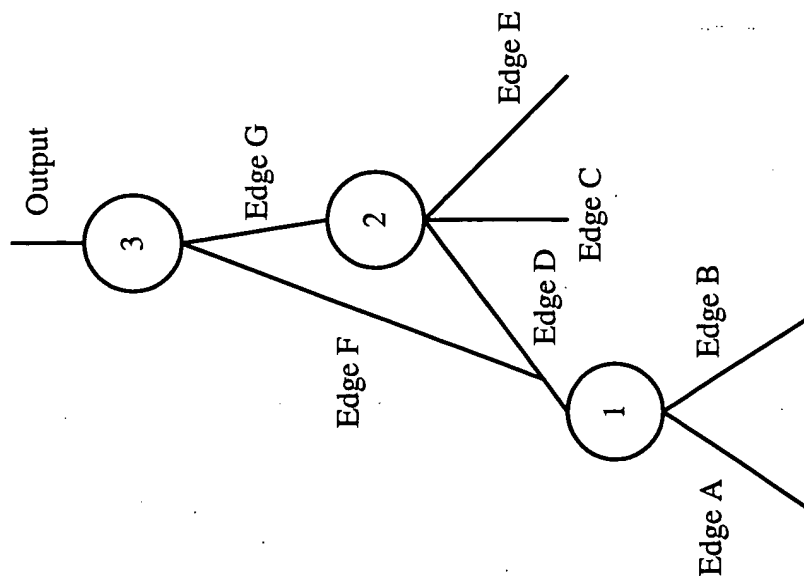


Figure 18

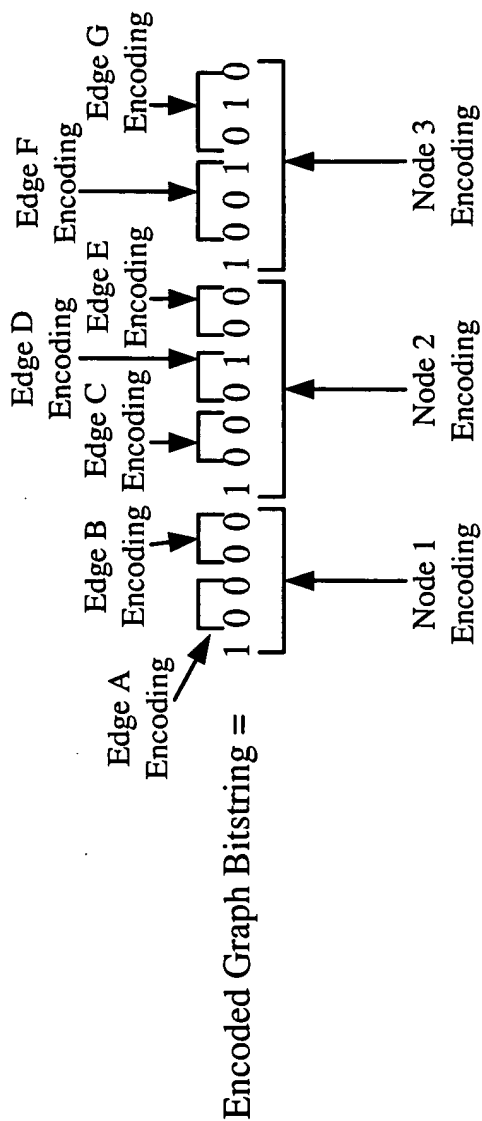


Figure 19

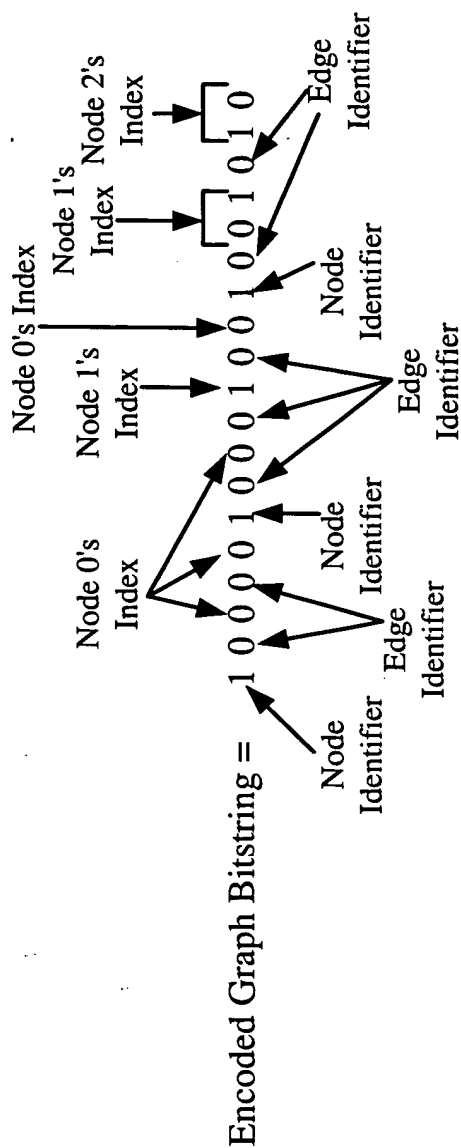


Figure 20

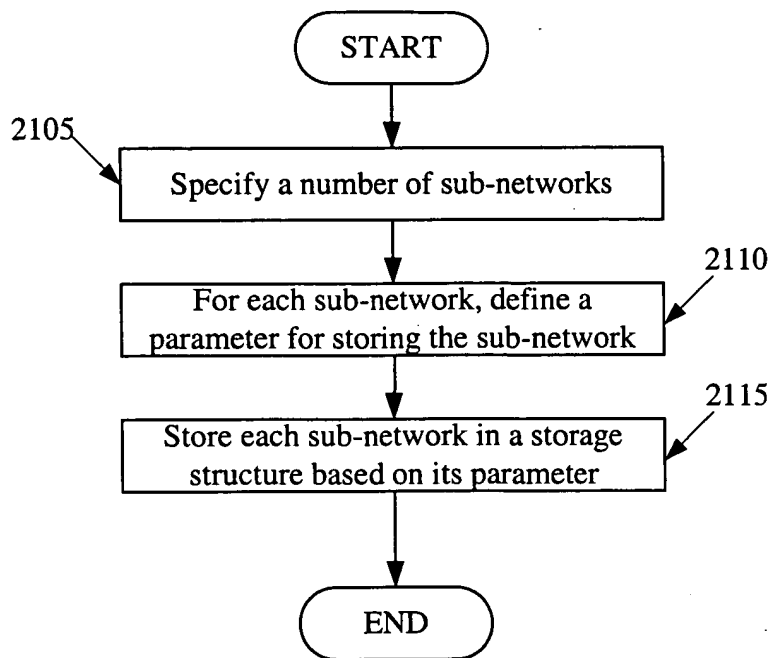


Figure 21

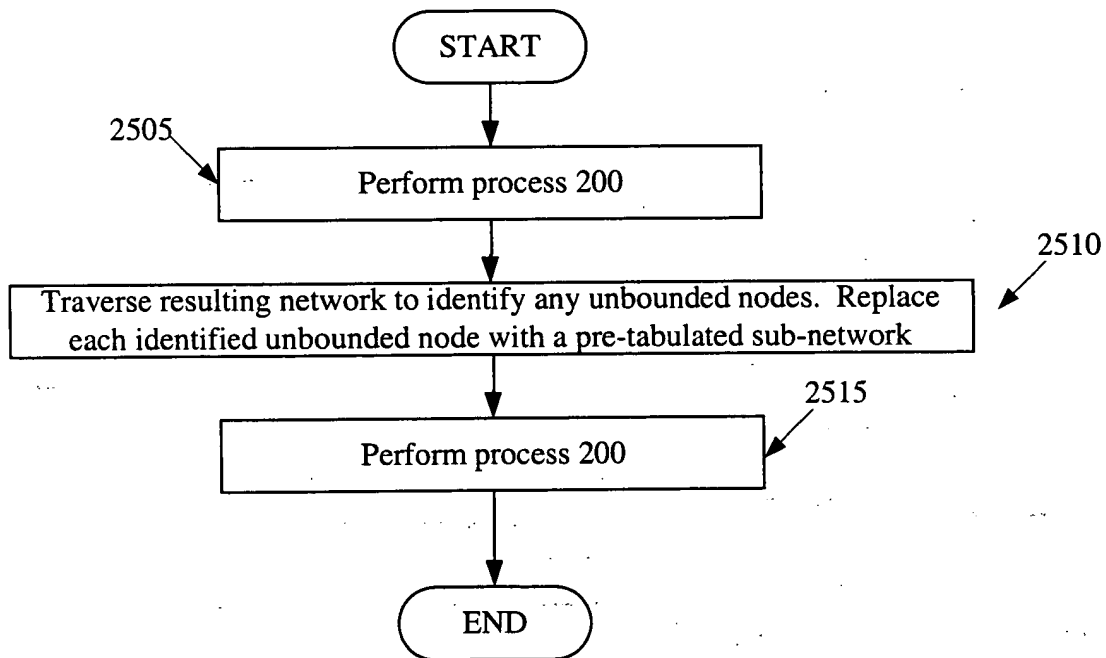


Figure 25

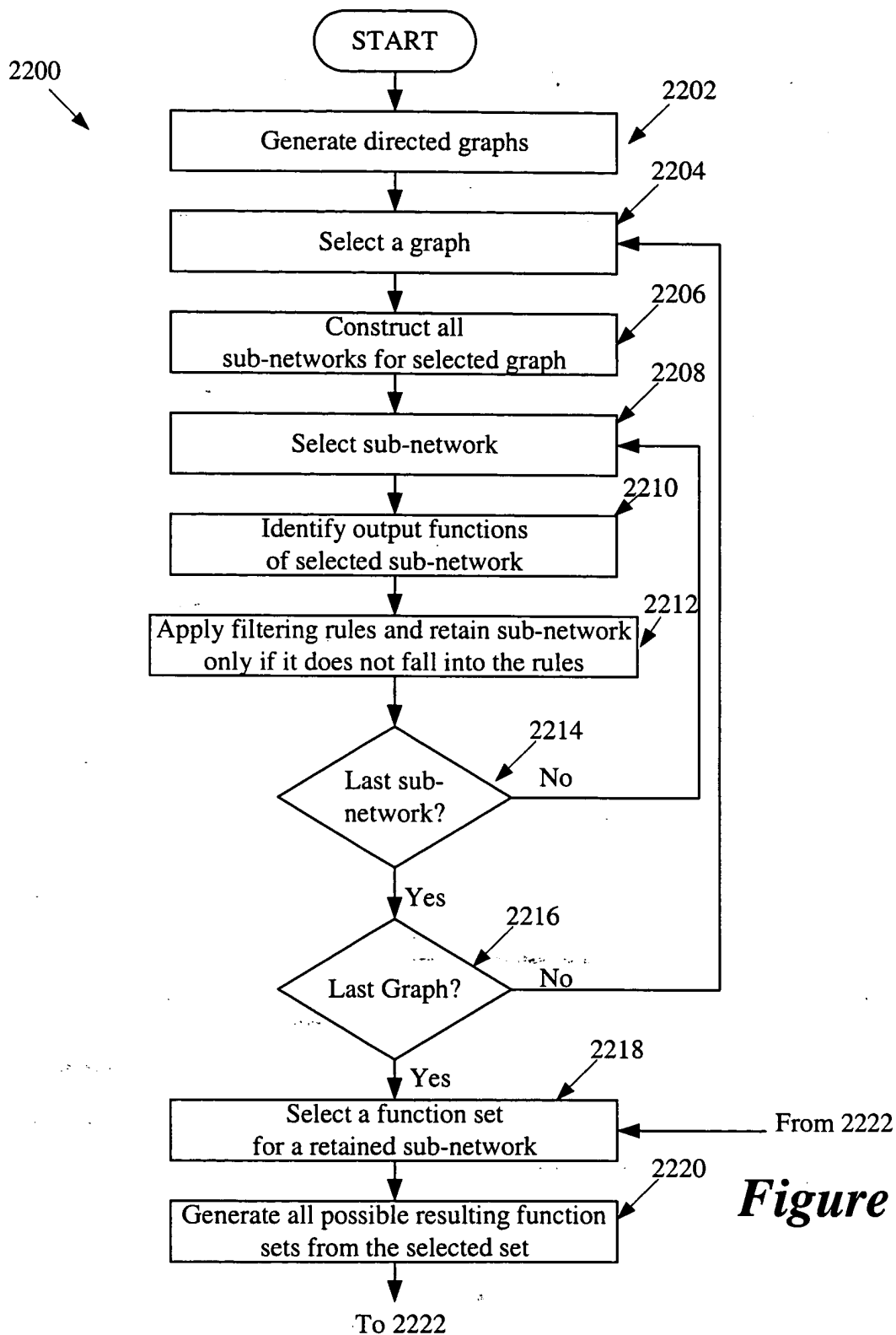


Figure 22A

Figure 22: Figure 22A
Figure 22B
Figure 22C

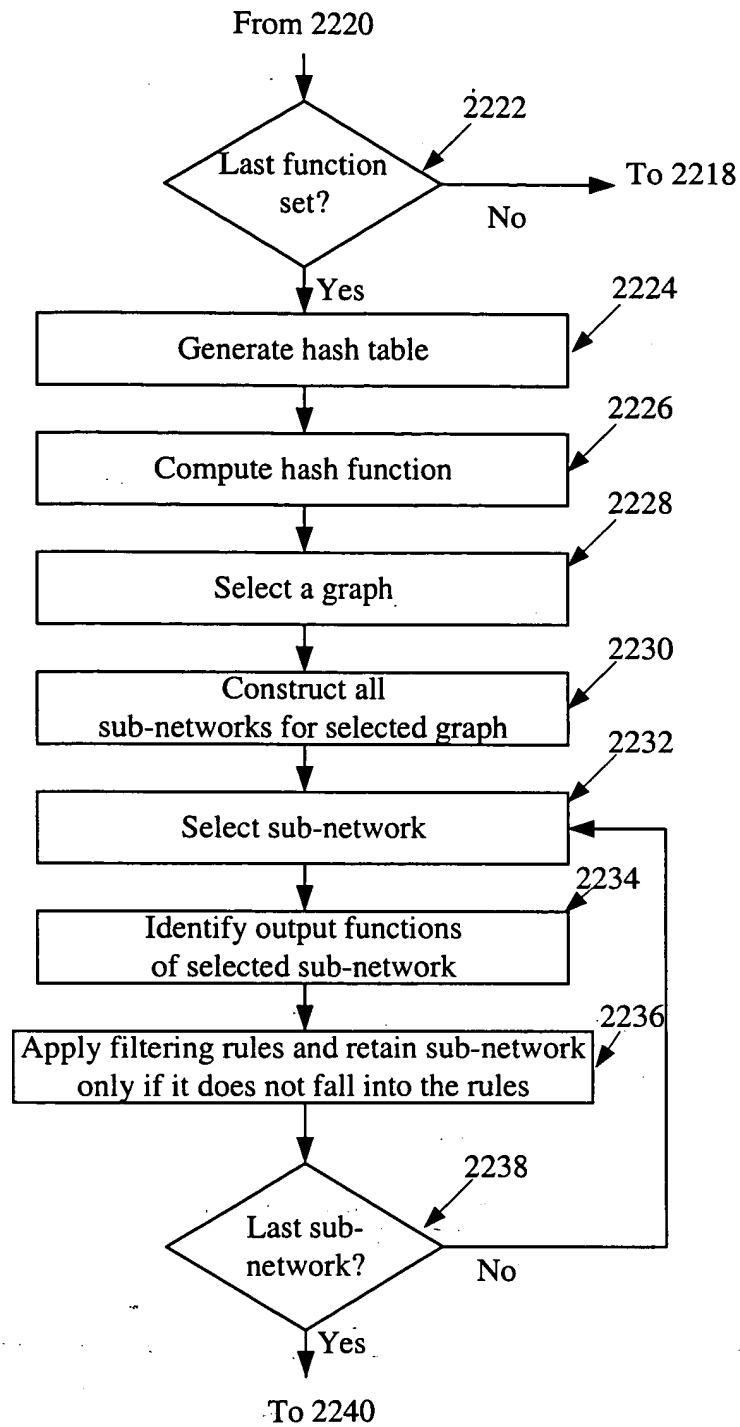


Figure 22B

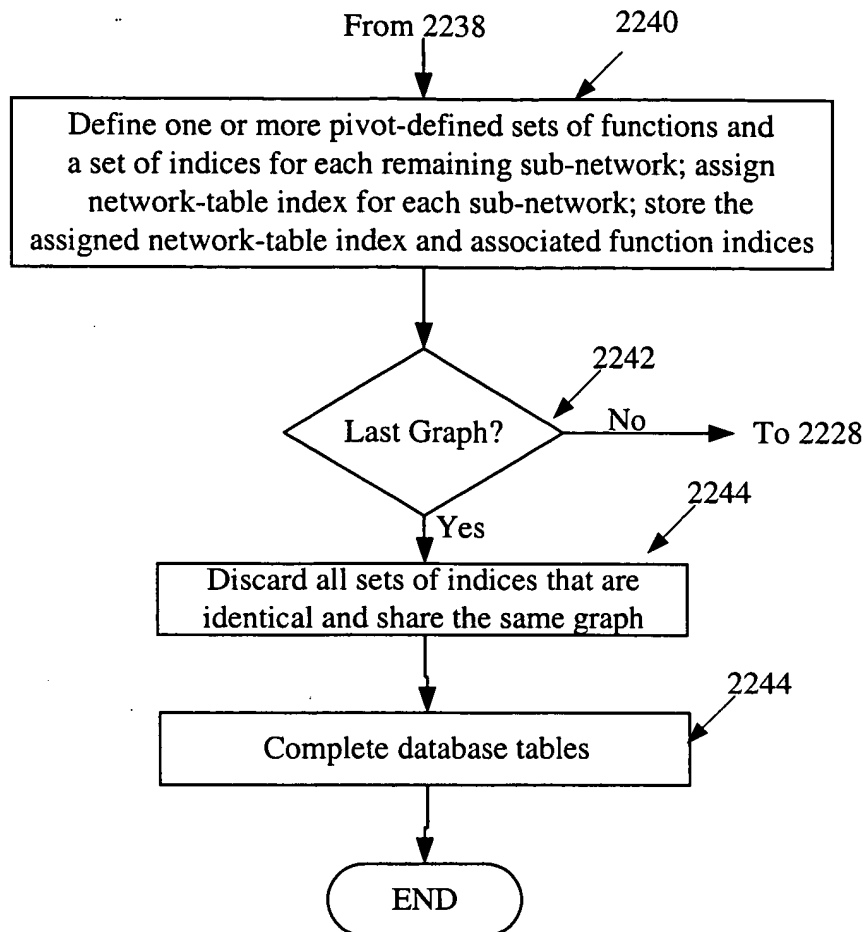


Figure 22C

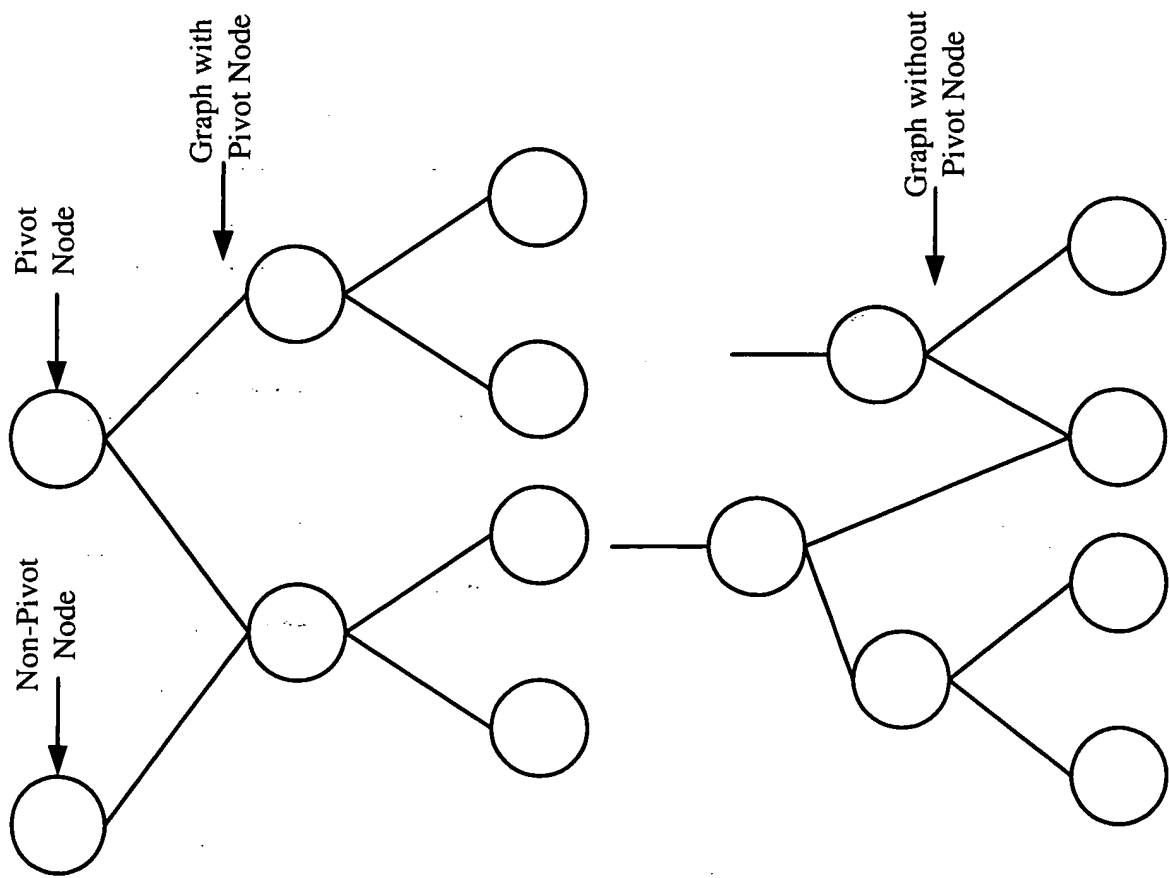


Figure 23

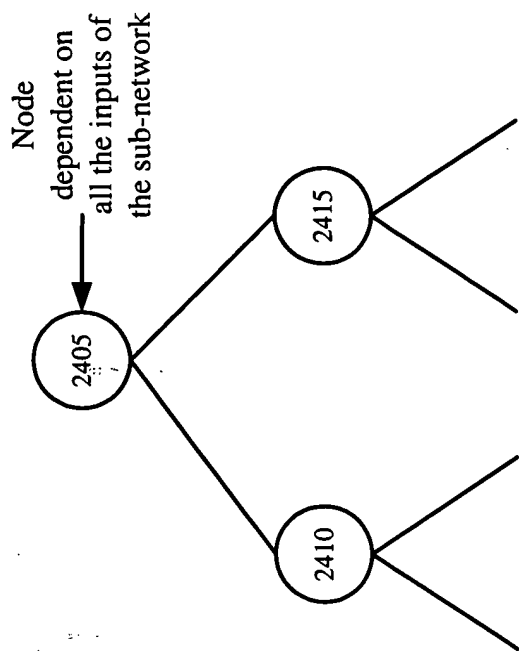


Figure 24

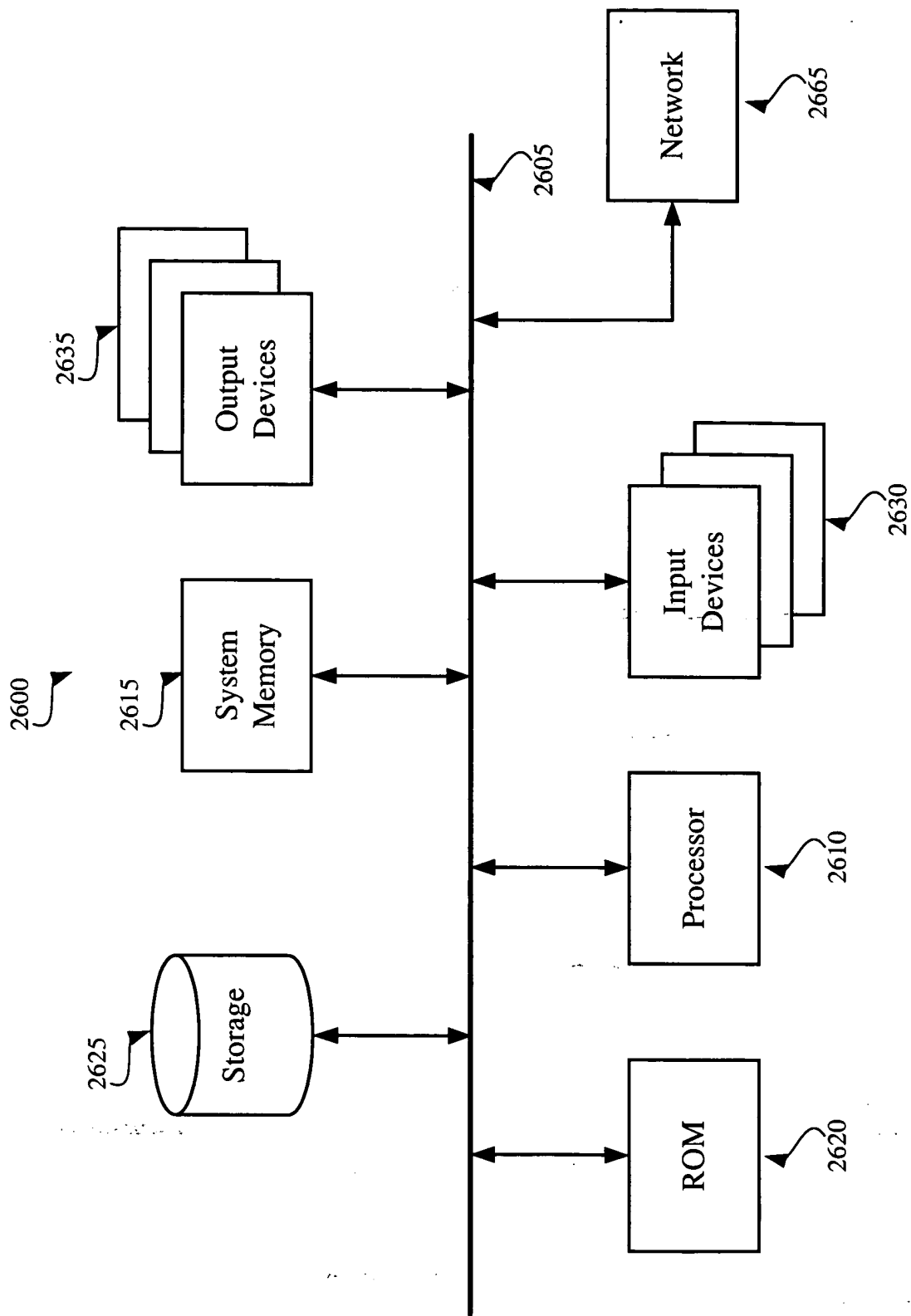


Figure 26